

Deploy Federated Event
Rodin Developer Tutorial
Programme
Monday 27th February

Time	
08:30 – 09:00	<u>Coffee</u>
	<p><u>Session 1:</u></p> <p><i>Using the Rodin Theory Plug-in - Issam Maamria (1h)</i></p> <p><i>Using the Rodin prover API to connect external provers</i> <i>SMT Solver Integration - Systereel (30 min)</i> <i>Isabelle/HOL integration - Matthias Schmalz (30 min)</i></p>
11:00 – 11:15	<u>Break</u>
	<p><u>Session 2:</u></p> <p><i>Creating and using custom/parameterized proof tactics</i> - Nicolas Beauger, Jean-Raymond Abrial (30 min) <i>Integrating plug-ins with ProB - Jens Bendisposto (30 min)</i></p>
12:15 – 14:00	<u>Lunch</u>
	<p><u>Master Class session 1 (1h30) in 4 Rooms:</u></p> <p>Room A : <i>Developping Theories</i> - Issam Maamria Room B : <i>Using custom/parameterized proof tactics</i> - Jean-Raymond Abrial, Nicolas Beauger, Thomas Muller Room C : <i>Parameterizing Isabelle with theories</i> - Matthias Schmalz Room D : <i>Developping for ProB</i> - Jens Bendisposto</p>
15:30 – 16:00	<u>Coffee</u>
	<p><u>Master Class session 2 (1h30) in 4 Rooms:</u></p> <p>Room A : <i>Developping Theories</i> - Issam Maamria Room B : <i>Using custom/parameterized proof tactics</i> - Jean-Raymond Abrial, Nicolas Beauger, Thomas Muller Room C : <i>Parameterizing Isabelle with theories</i> - Matthias Schmalz Room D : <i>Developping for ProB</i> - Jens Bendisposto</p>



Deploy Federated Event
Rodin User & Developer Workshop
Programme
Tuesday 28th February
Day One

Time	
08.30 - 09.00	<u>Coffee</u>
09.00 - 10.30	<u>Session 1:</u> Welcome <i>SafeCap Modelling Environment</i> - Alexei Iliasov <i>Verification of a Railway Interlocking UML Model</i> <i>Translation to UML-B</i> - Gintautas Sulskus & Colin Snook <i>Component Reification in System Modelling</i> - Jens Bendisposto & Stefan Hallerstede
10.30 - 11.00	<u>Break:</u>
11.00 - 12.30	<u>Session 2:</u> <i>Code Generation Update</i> – Andrew Edmunds C.J.Lovell, R. Silva, I.Maamria & M.Butler <i>Ensuring Extensibility with Code Generation</i> – Chris Lovell, A. Edmunds, R.Silva, I. Maamria & M. Butler <i>Towards a Certifying Code Generator for Rodin</i> – Alexei Iliasov <i>Generating Executable Simulations from Event-B Specifications</i> – Faqing Yan, Jean-Pierre Jacquot, and Jeanine Souquières
12.30 - 13.30	<u>Lunch:</u>
13.30 - 15.30	<u>Session 3:</u> <i>Fault Tolerance Views</i> – Ilya Lopatkin, Alexei Iliasov, Alexander Romanovsky <i>Use of Rodin in FDIR Architecture for Autonomous Systems</i> – Jean-Charles Chaudemar <i>Pattern for Modelling Fault Tolerant in Event-B</i> – Michael Poppleton & Gintautas Sulskus <i>Extending Event-B & Rodin with Discrete Timing Properties</i> – Reza Sarshogh & Michael Butler
15.30 - 16.00	<u>Coffee:</u>
16.00 - 17.30	<u>Session 4:</u> <i>A Framework for Diagrammatic Modelling Extensions in Rodin</i> – Vitaly Savicks & Colin Snook <i>Systematic Development for Embedded Systems Design using RRM Diagrams and UML-B</i> – Manoranjan Satpathy, Colin Snook, Silky Arora <i>CODA: A formal Event-B based Refinement Framework for High Integrity Embedded System Development</i> – John



	<p>Colley, Michael Butler, Colin Snook, Neil Evans, Neil Grant & Helen Marshall <i>ADVANCE: Advanced Design & Verification Environment for Cyber-Physical Systems Engineering – The Multi-Simulation Framework</i> – John Colley & Michael Butler</p>
--	-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------



Deploy Federated Event
Rodin User & Developer Workshop
Programme

Wednesday 29th February
Day Two

Time	
08.30 - 09.00	<u>Coffee</u>
09.00 - 10.30	<p><u>Session 5:</u></p> <p><i>Proving Consensus</i> - Jeremy Bryans & Alexei Iliasov <i>Verification and validation of BPEL processes - A proof and animation based approach</i> - Idir Ait-Sadoune, Yamin Ait-Ameur & Mickael Baron <i>Formal Specification of a Mobile Diabetes Management Application Using the Rodin Platform and Event-B</i> - Daniel Brown, Ian Bayley, Rachel Harrison, Clare Martin <i>Synthesis of Processor Instruction Sets from High-Level ISA Specifications</i> - Andrey Mokhov, Alexei Iliasov Danil Sokolov, Maxim Rykunov, Alex Yakovlev, Alexander Romanovsky</p>
10.30 - 11.00	<u>Break:</u>
11.00 - 12.30	<p><u>Session 6:</u></p> <p><i>An Event-B Plug-in for Creating Deadlock-Freeness Theorems</i> - Faquing Yang & Jean-Pierre Jacquot <i>The Theory plug-in and its Applications</i> - Issam Maamria & Michael Butler <i>Can rippling discover the missing lemmas for invariant proofs?</i> - Gudmund Grov, Yuhui Lin & Alan Bundy <i>Proof Hints for Event-B Models - Extended Abstract</i> - Thai Son Hoang</p>
12.30 - 13.30	<u>Lunch:</u>
13.30 - 15.30	<p><u>Session 7:</u></p> <p><i>Requirements Traceability between Textual Requirements and Event-B Using ProR</i> - Michael Jastram, Lukas Ladenberger & Michael Leuschel <i>Towards Relating Sub-Problems of a Control System to Sub-Models in Event-B</i> - Sanaz Yeganefard & Michael Butler <i>Lessons from Deployment</i> - Manuel Mazzara, Cliff Jones & Alexei Iliasov <i>Assessment of the Evolution of the RODIN Open Source platform</i> - Christophe Ponsard, Jean Christophe Deprez, Jacques Flamand</p>
15.30 - 16.00	<u>Coffee:</u>



16.00 - 17.30

Session 8:

A Rodin Plugin for automata learning and test generation for Event-B – Ionut Dinca, Florentin Iplate, Laurentiu Mierla & Alin Stefanescu

VTG - Vulnerability Test cases Generator, a Plug-in for Rodin - Aymerick Savary, Jean-Louis Lanet Marc Frappier & Tiana Razafindralambo

Visualisation of LTL Counterexamples with ProB – Andriy Tolstoy, Daniel Plagge & Michael Leuschel.